

L14

- Scheduling
- Cache problem

4102

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Scheduling

	start	end
sy333	<u>2</u>	<u>3.25</u>
en162	1	4
ma123	3	4
cs4102	3.5	4.75
cs4402	4	5.25
cs6051	4.5	6
sy333	5	6.5
cs1011	7	8

problem statement

$(a_1, \dots, a_n) \leftarrow n$ activities

(s_1, s_2, \dots, s_n) start times

finish times

$\rightarrow (\underline{f_1}, \underline{f_2}, \dots, \underline{f_n})$ (sorted) $\underline{s_i} < \underline{f_i}$

find largest subset of activities $\underline{C} = \{a_i\}$ such that
(compatible)

for any two $a_i, a_j \in C$ s.t. $i < j$

$$f_i < s_j$$

problem statement

$$(a_1, \dots, a_n)$$

$$(s_1, s_2, \dots, s_n)$$

$$(f_1, f_2, \dots, f_n) \text{ (sorted)} \quad s_i < f_i$$

find largest subset of activities $C = \{a_i\}$ such that
(compatible)

$$a_i, a_j \in C, i < j$$

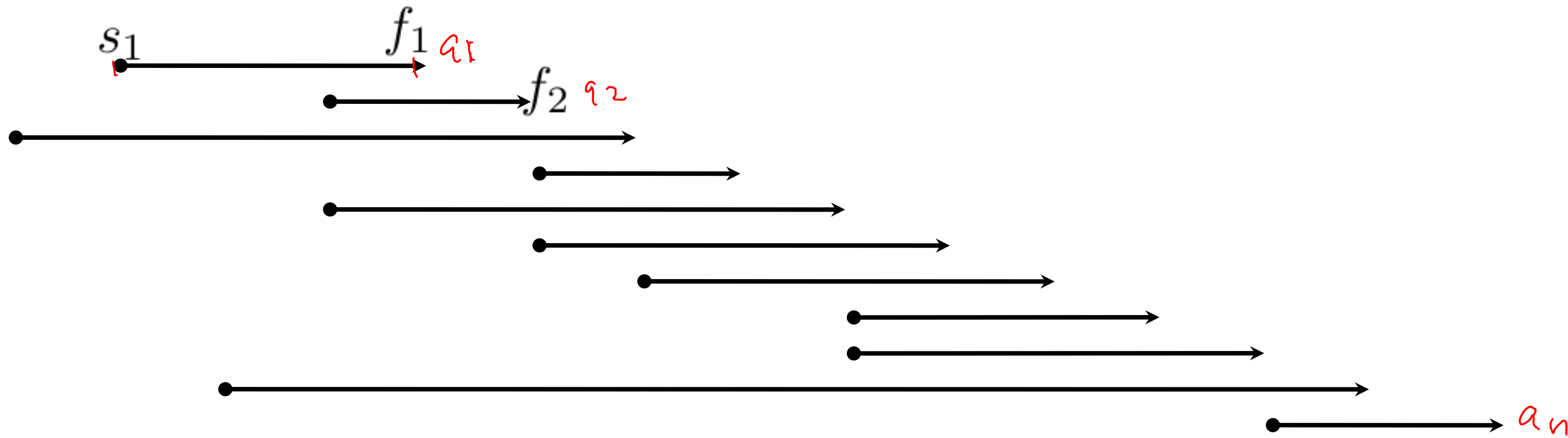
$$f_i \leq s_j$$

problem statement

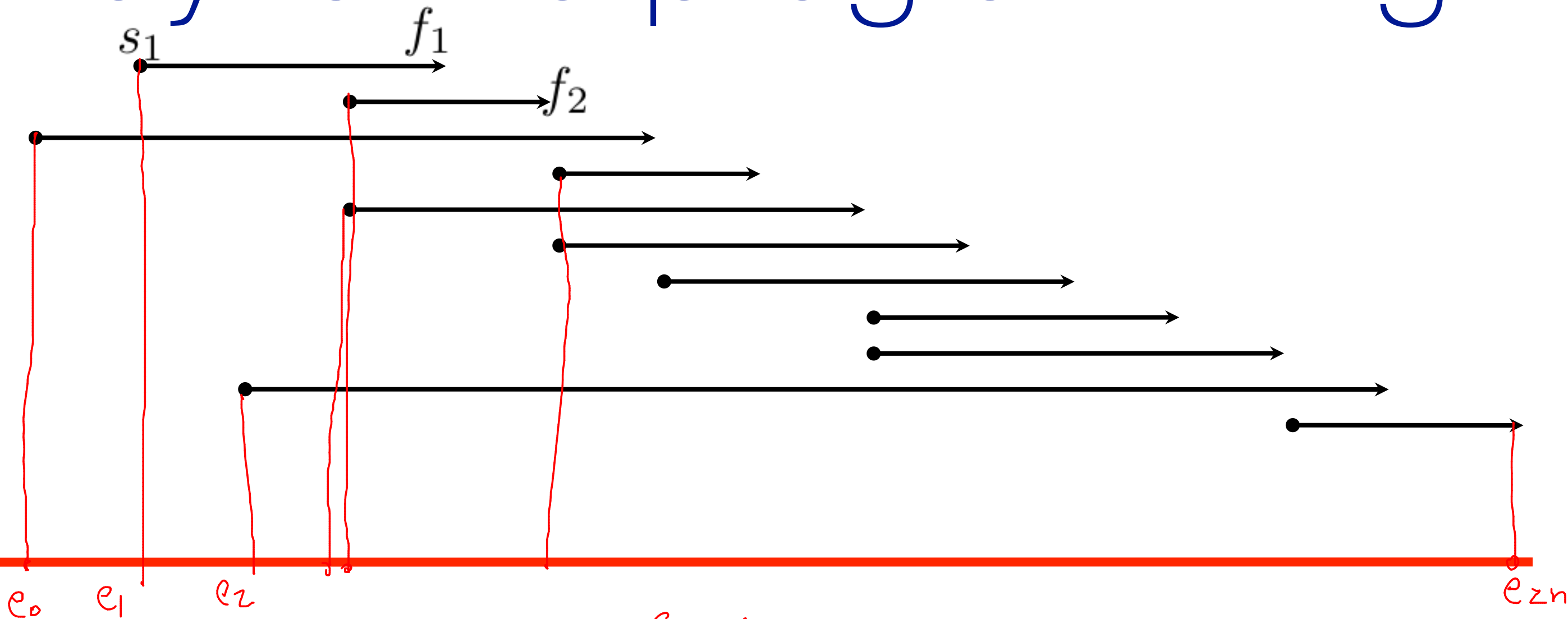
(a_1, \dots, a_n)

(s_1, s_2, \dots, s_n)

(f_1, f_2, \dots, f_n) (sorted) $s_i < f_i$

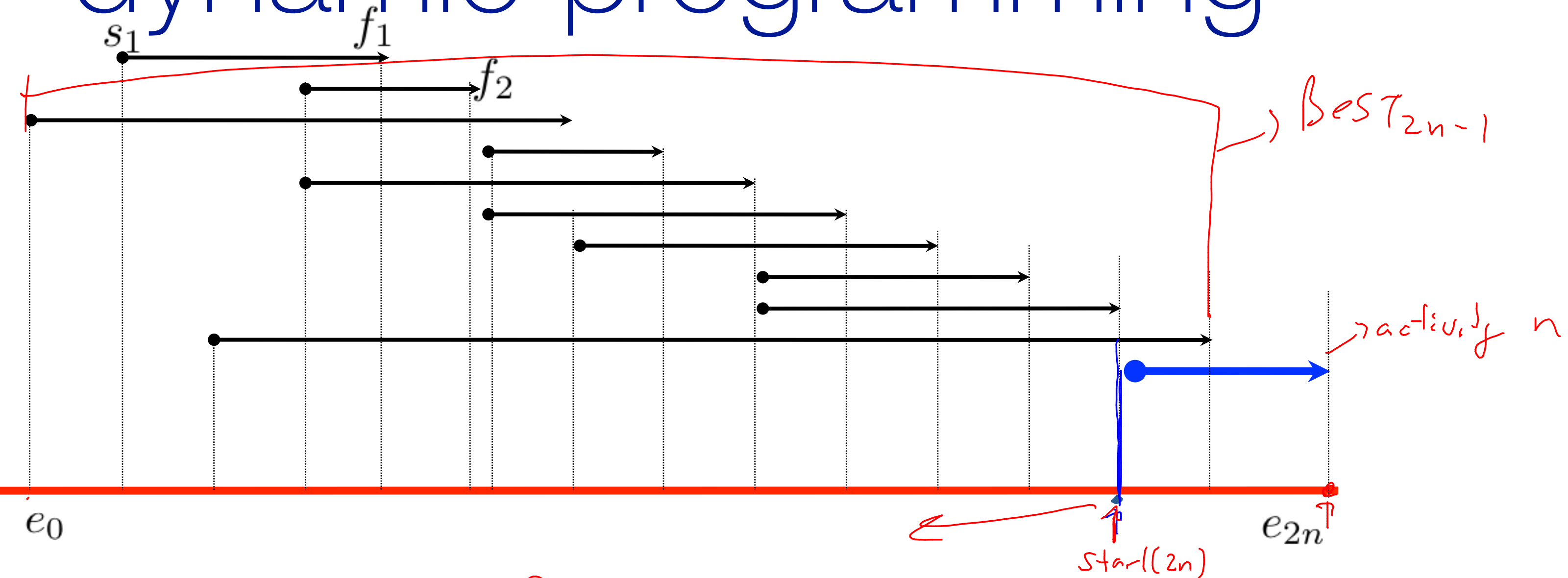


dynamic programming



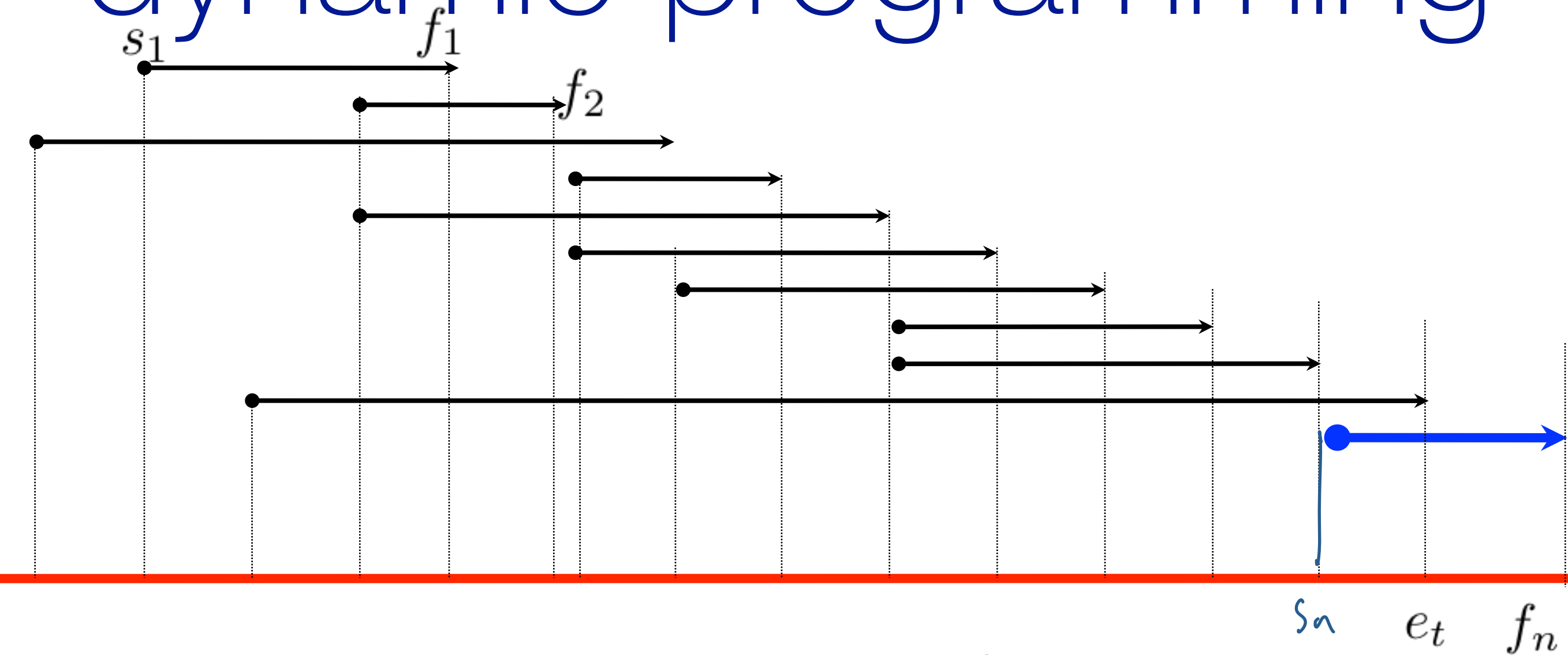
B_{est}^n = maximal # of activities that can occur before event n .

dynamic programming



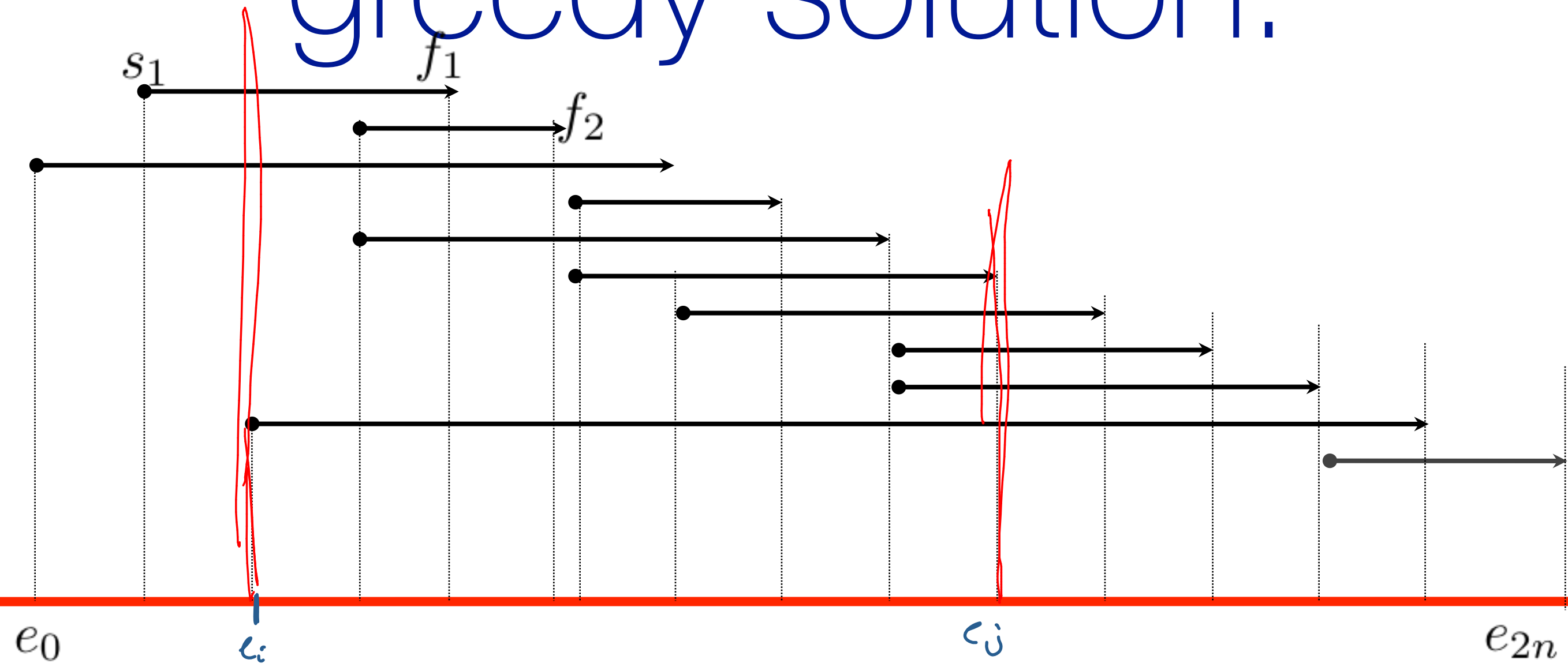
$$Best_{zn} = \max \left\{ \begin{array}{l} 1 + Best_{start(zn)} \\ Best_{zn-1} \end{array} \right.$$

dynamic programming



$$\text{BEST}_{f_n} = \max \begin{cases} \text{BEST}_{s_n} + 1 & \text{in: } a_n \\ \text{BEST}_{e_t} & \text{out: } a_n \end{cases}$$

greedy solution:

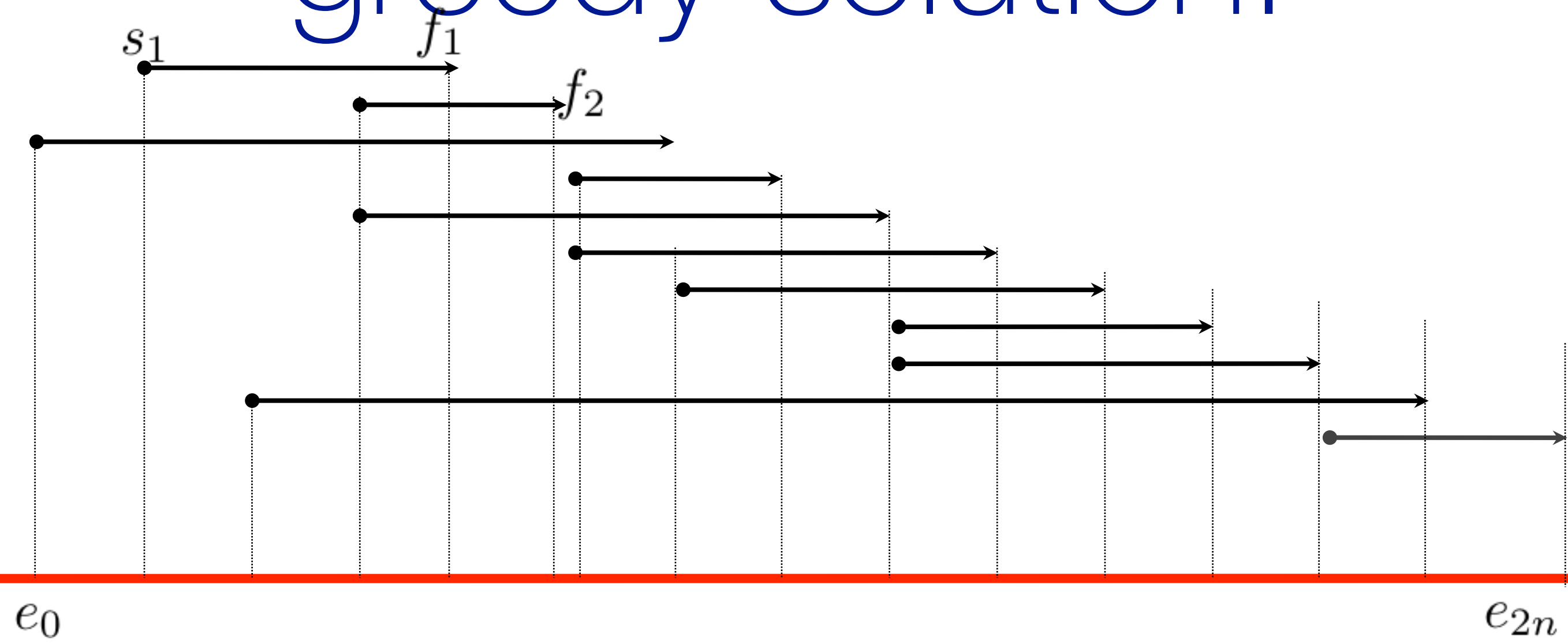


definition:

$\text{SOLTN}_{i,j}$ = maximal # of activities that occur between events i, j .

goal: $\text{SOLTN}_{0,2n}$

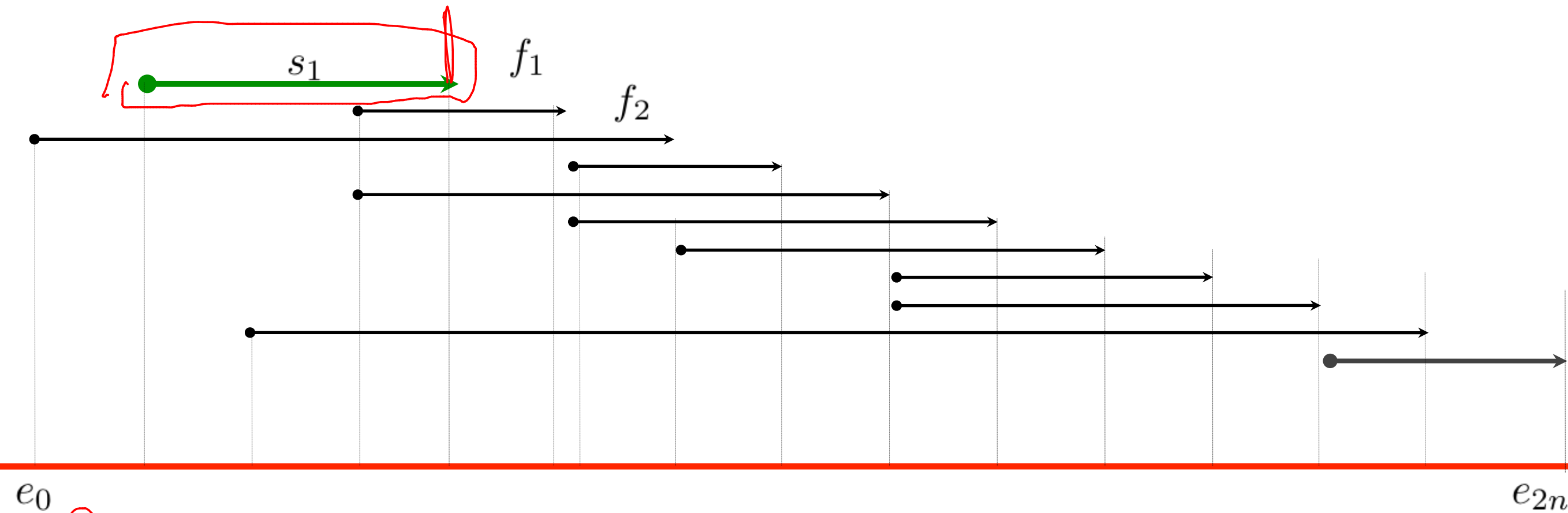
greedy solution:



SOLTN $_{i,j}$

goal: SOLTN $_{0,2n}$

greedy solution:



claim: the first action to finish in $e[i,j]$ is always part of some $SOLTN_{i,j}$ \Rightarrow optimal solution for period $[i,j]$

$\Rightarrow a_i$ is always part of some $SOLUTN_{0,2n}$

claim: the first action to finish in $e[i,j]$ is always part of some $SOLTN_{i,j}$

(Exchange arguments)

proof: Consider $SOLTN_{i,j}$ and let a^* be the first activity to finish in $[i,j]$.

(1) If $a^* \in SOLTN_{i,j}$, then the claim follows.

(2) Suppose that $a^* \notin SOLTN_{i,j}$. Let activity a be the first activity to finish in $SOLTN_{i,j}$.

a^* finishes before a , i.e. $f_{a^*} \leq f_a$ by hypothesis.

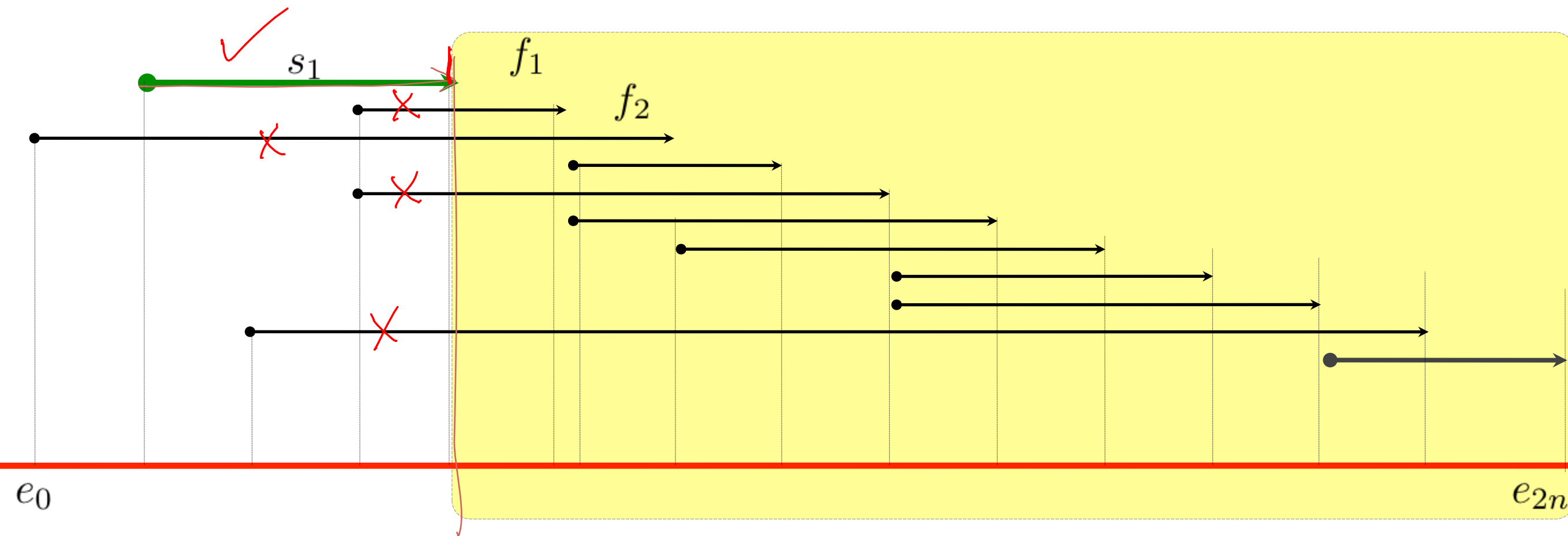
So therefore,

$$S = SOLTN_{i,j} - \{a\} + \{a^*\}$$

then $|S| = |SOLTN_{i,j}|$ and so S is optimal too.

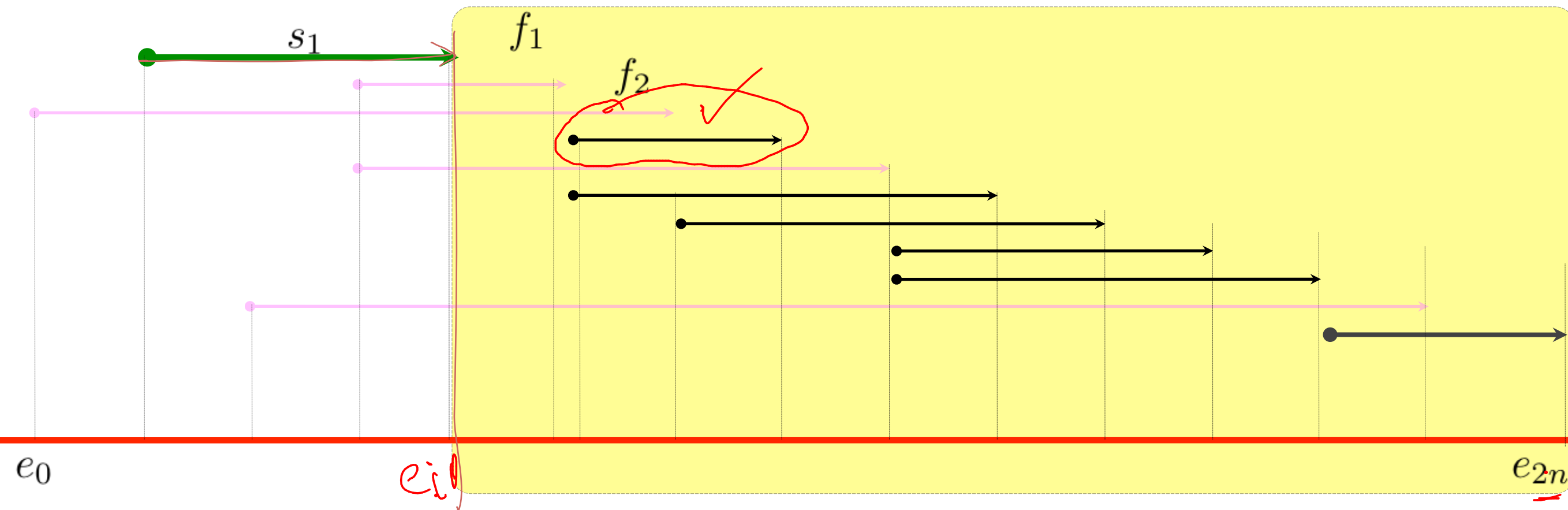
Therefore the lemma follows.

greedy solution:



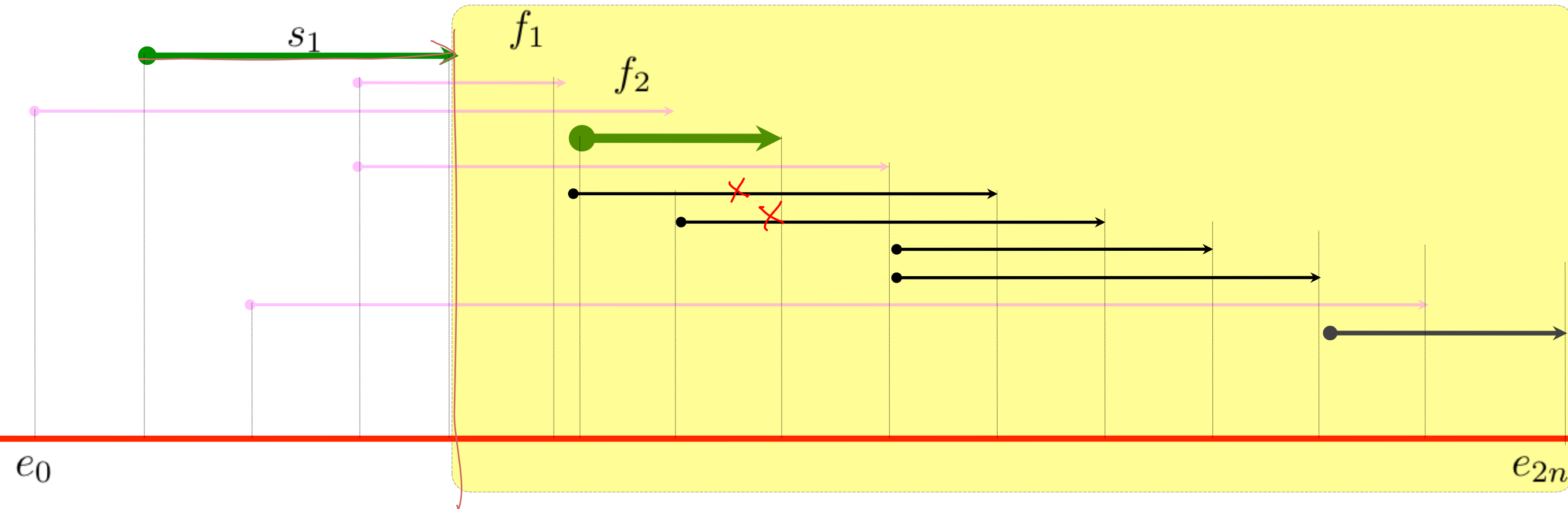
algorithm: find first event to finish. add to solution.
remove conflicting events.
continue.

greedy solution:



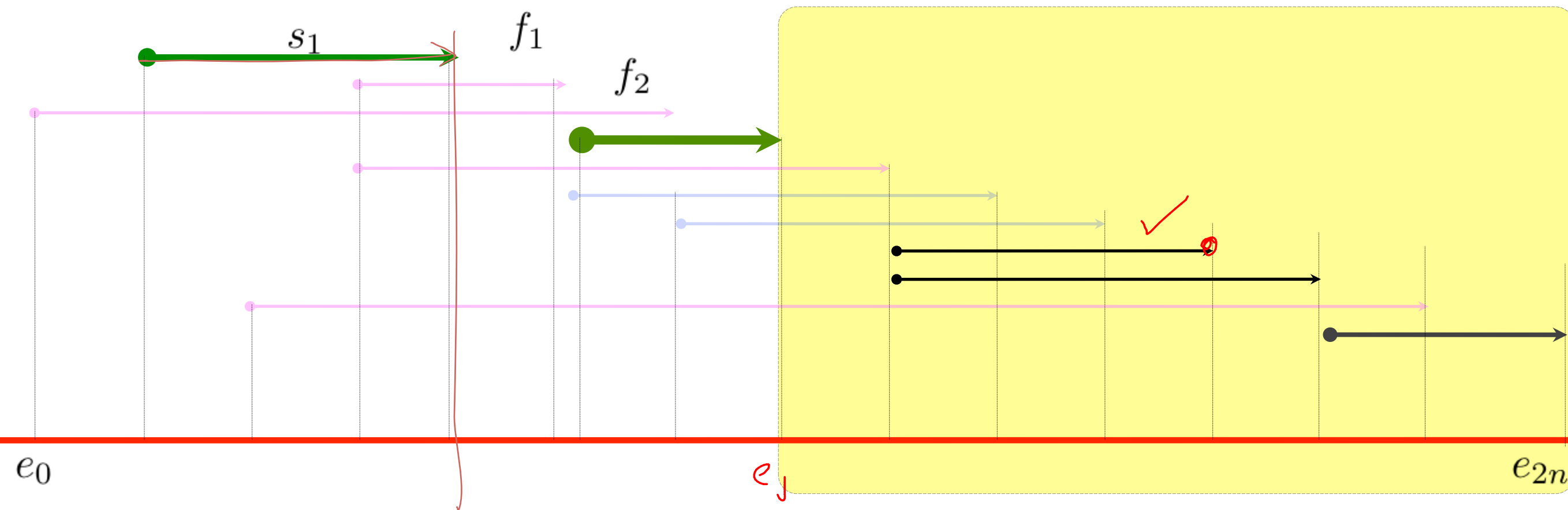
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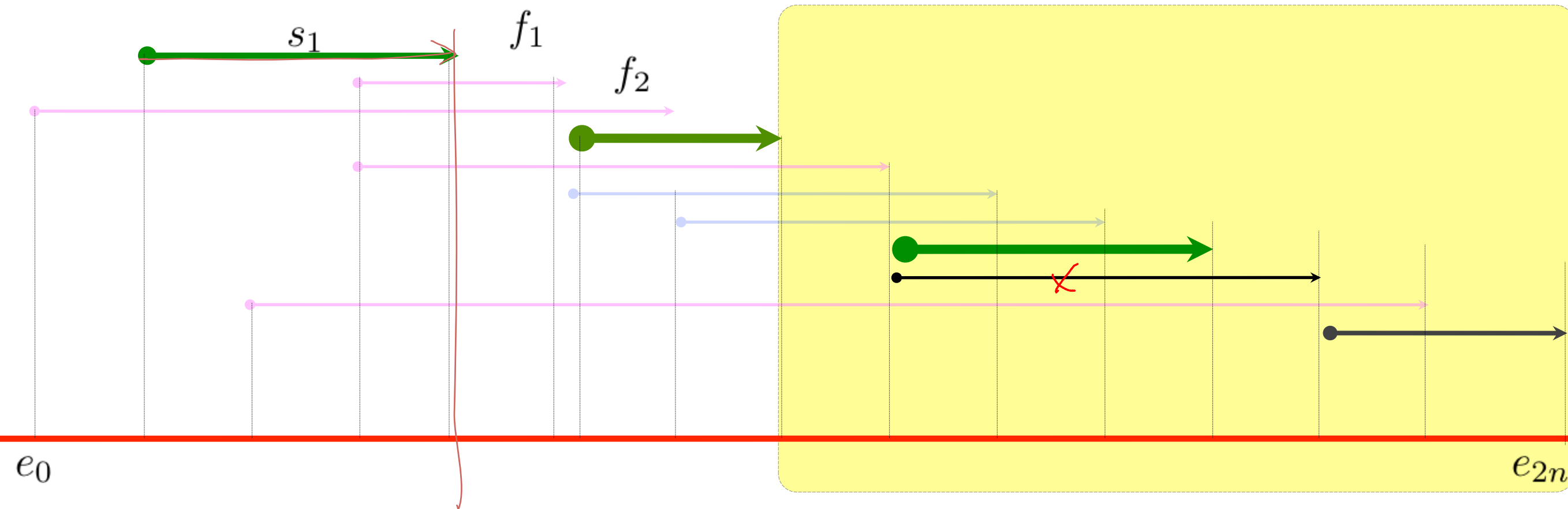
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greedy solution:



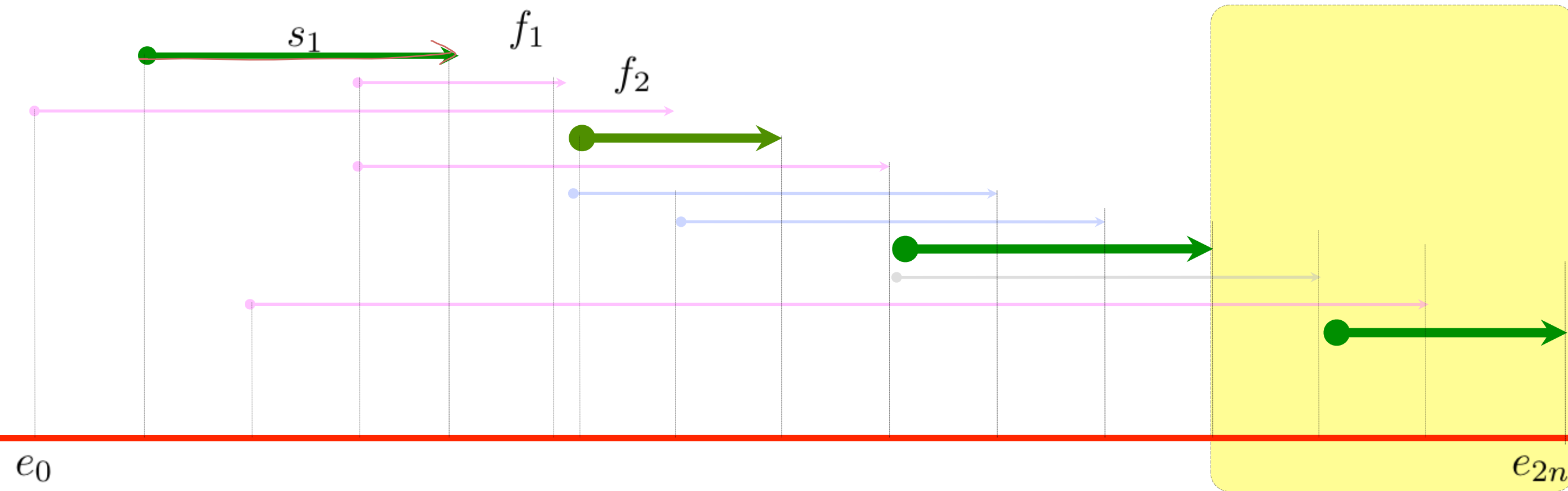
algorithm: find first event to finish. add to solution.
remove conflicting events.
continue.

greedy solution:



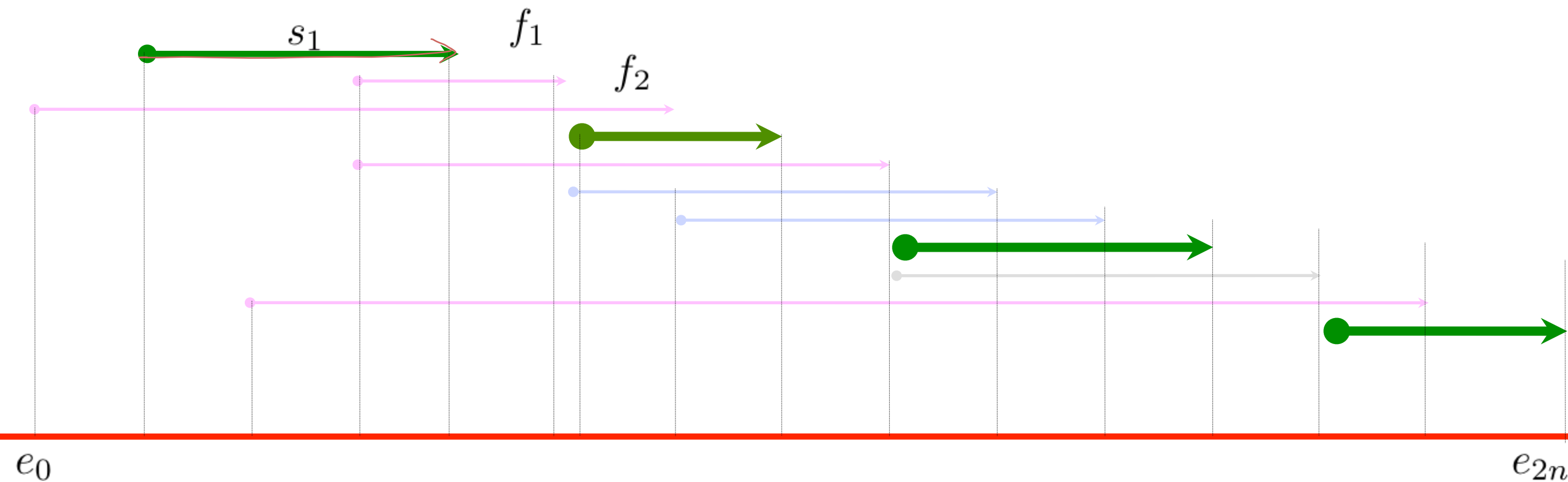
algorithm: find first event to finish. add to solution.
remove conflicting events.
continue.

greedy solution:



algorithm: $\left\{ \begin{array}{l} \text{find first event to finish. add to solution.} \\ \text{remove conflicting events.} \\ \text{continue.} \end{array} \right.$

greedy solution:



algorithm: find first event to finish. add to solution.
remove conflicting events.
continue.

running time

algorithm: find first event to finish. add to solution.
remove conflicting events.
continue.

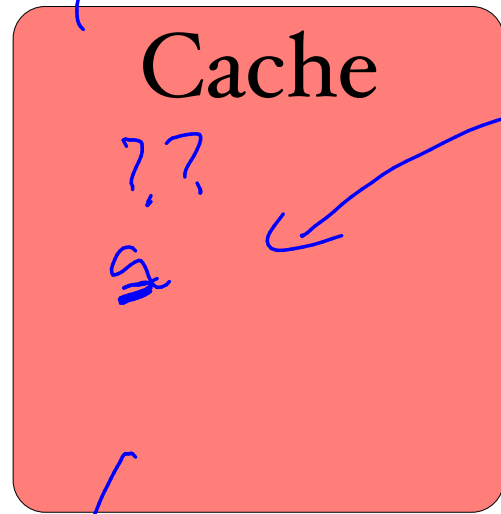
$\Theta(n)$

(f_1, f_2, \dots, f_n) (sorted) $s_i < f_i$

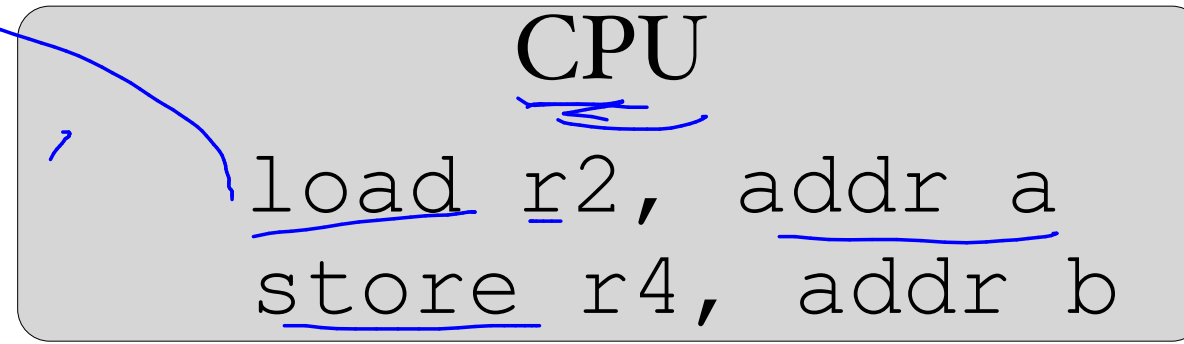
caaching

cache hit

small cache
very fast

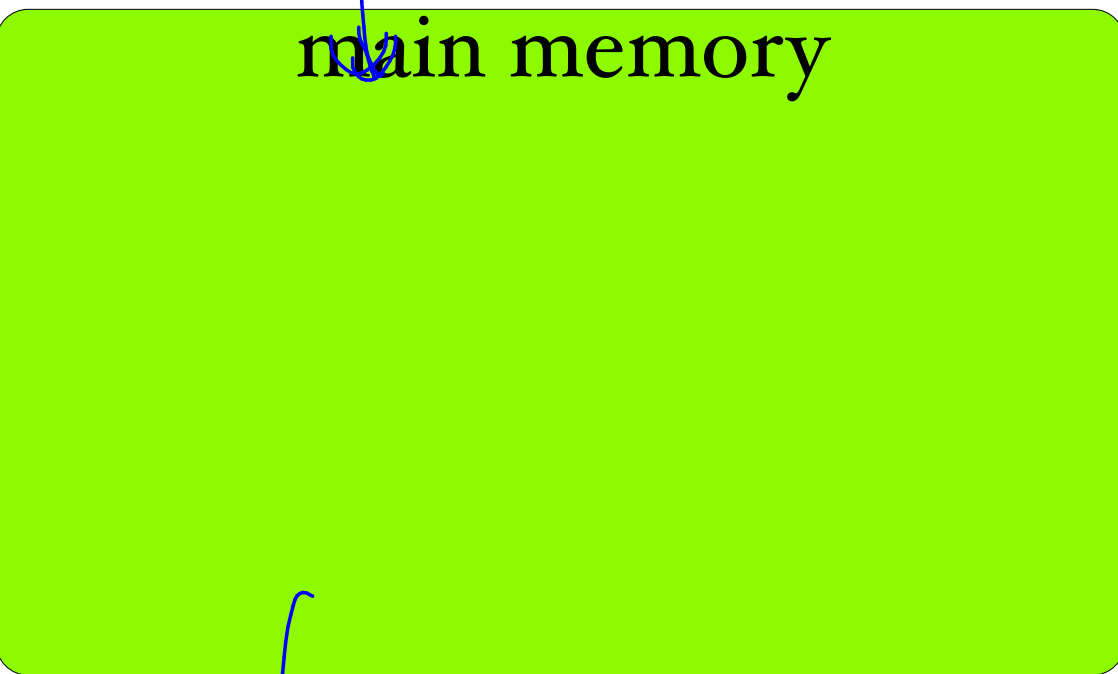


fast

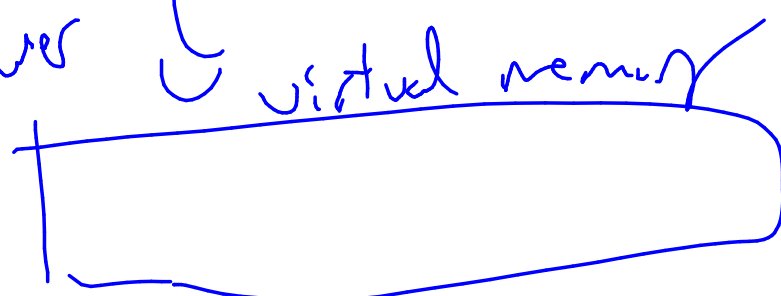


slow

main memory



slower



question:

① How can we manage a cache in order to minimize # of cache misses.

* SIMPLIFY by assuming that we know all memory accesses before hand

* cache is fully associative, line size = 1.

problem statement

input: K - cache size, $d_1, d_2, d_3, \dots, d_n$ memory access pattern.

output: schedule for the cache

cache is

problem statement

input: K , the size of the cache
 d_1, d_2, \dots, d_m memory accesses

output: schedule for that cache that minimizes # of cache misses while satisfying requests

cache is fully associative, line size is 1

contrast with reality

① caches are not fully associative

② ~~*~~!!! the manager does not know
the future access pattern

Belady evict rule

"Evict the item from the cache that is accessed farthest in the future"

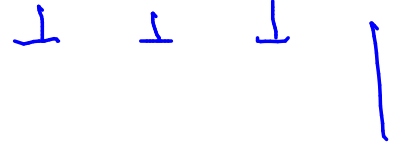
→ Using the ff rule results in an optimal schedule.

example

cache



a b c d a d e a d b a e c e a

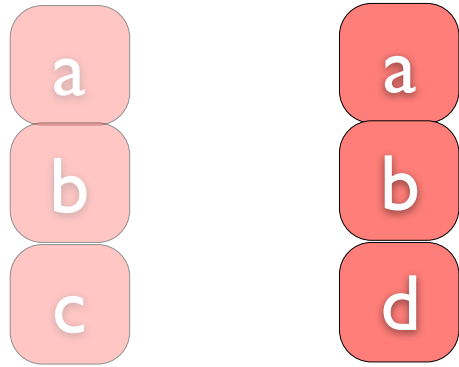


"evict c for d."

← memory access pattern

example

cache



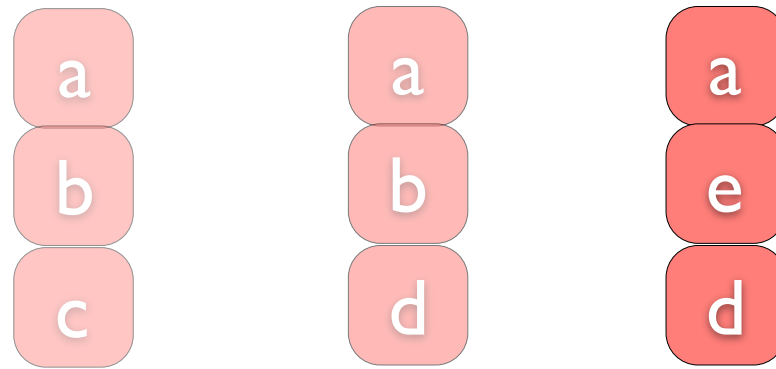
a b c d a d e a d b a e c e a



"evict b for e"

example

cache



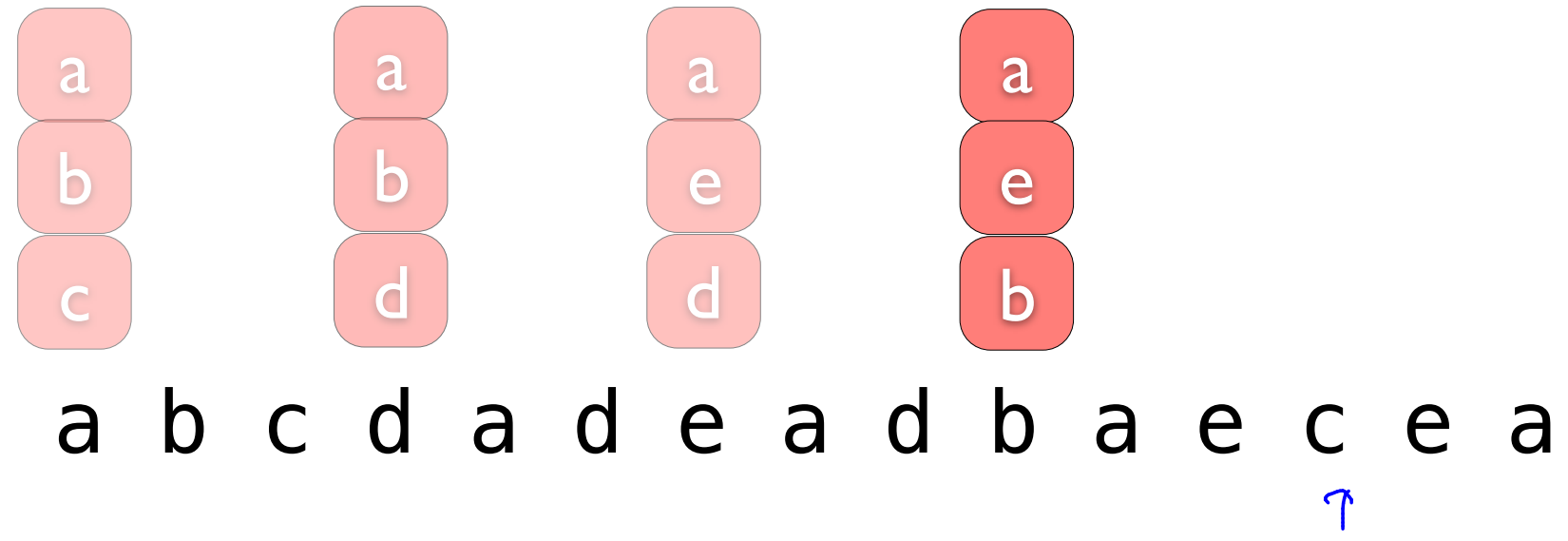
a b c d a d e a d b a e c e a



“evict d for b”

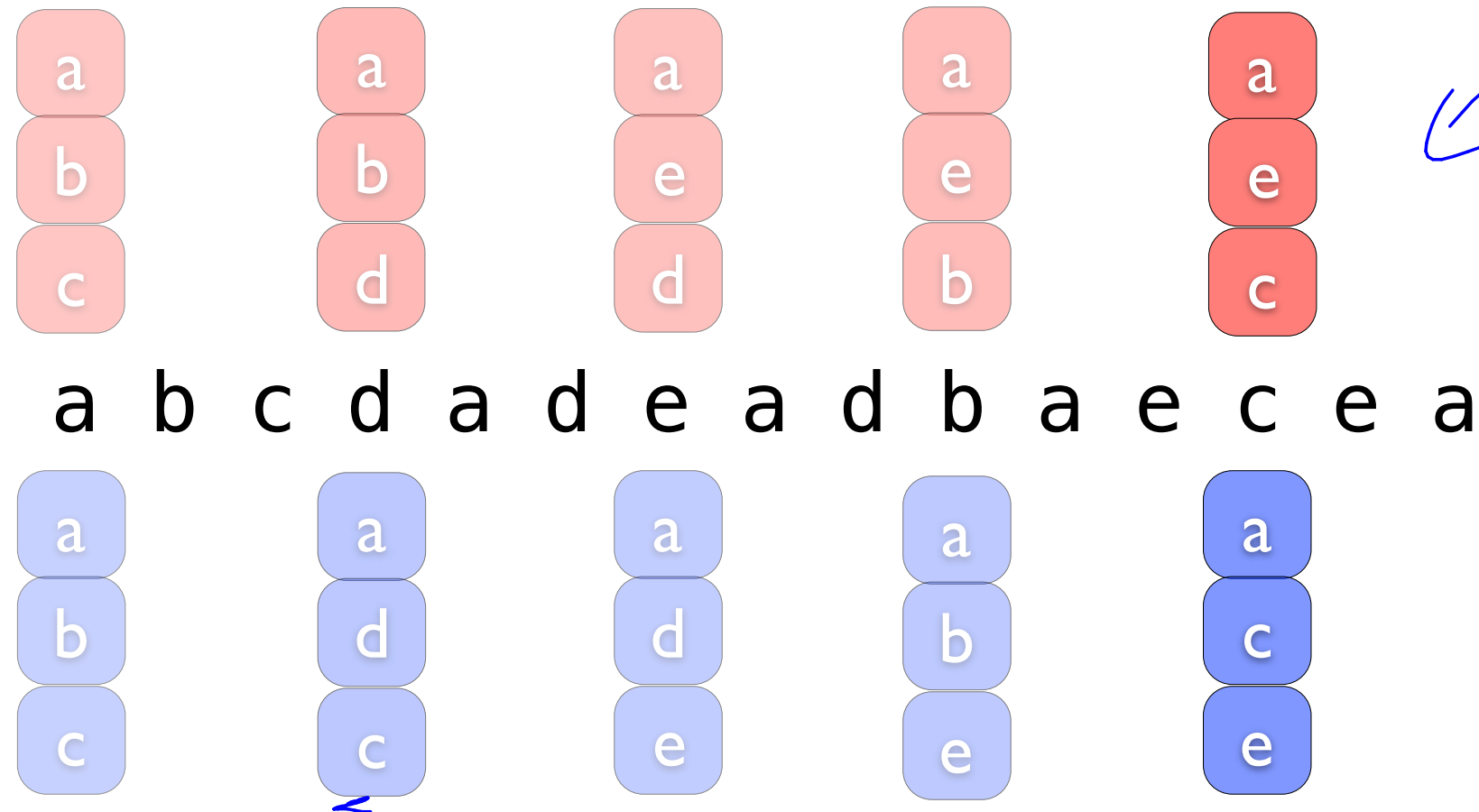
example

cache



example

cache



Belady eviction rule.
4 cache misses

↑ another schedule.

Not ~~FF~~

4 cache misses.

Surprising theorem

- Belady eviction rule leads to an optimal schedule.

schedule

Schedule for access pattern d_1, d_2, \dots, d_n : either the 'NOP' operation or the 'evict x for y ' operation @ each step $1 \dots n$.

- At step i , element d_i must be in the cache.

lazy
Reduced schedule: Schedule for which the operation "Evict x for y " only occurs at a step i if $y = d_i$.

Exchange lemma

Belady schedule.

Let ^{reduced} schedule S agree with schedule S_{FF} for the first j operations.

There exists a reduced schedule S' that agrees with S_{FF} on the first $j+1$ operations and

$$\underline{\#misses(S')} \leq \#misses(S).$$

Exchange Lemma:

Let S be a reduced schedule that agrees with S_{ff} on the first j items. There exists a reduced schedule S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

Optimal schedule.

S_0^*

lemma

S_1

lemma

S_2

$\rightarrow \rightarrow$

S_{ff}

$= \# \text{misses}(S_{ff})$

$\# \text{misses}(S^*)$

S_1 agrees with S_{ff} on \perp operation

$$\# \text{misses}(S_1) = \# \text{misses}(S^*) = \# \text{misses}(S_2)$$

Proof of Lemma

Let S be a reduced sched that agrees with S_{ff} on the first j items.

There exists a reduced sched S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

Proof: Since S agrees with S_{ff} on the first j operations, then the state of the cache @ operation $j+1$ will be the same. Let d be the address accessed @ ^{this} operation $j+1$.

Proof of lemma

operation $j+1$ accesses d .

State of the cache after J operations under the two schedules.



easy case 1 $S_{pse} \xrightarrow{d \in \text{cache}}$ Then $S' = S$ b/c both S and S_{ff} issue "NOP."

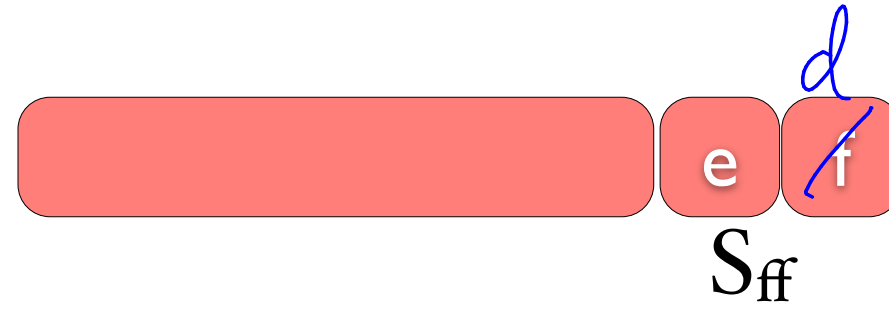
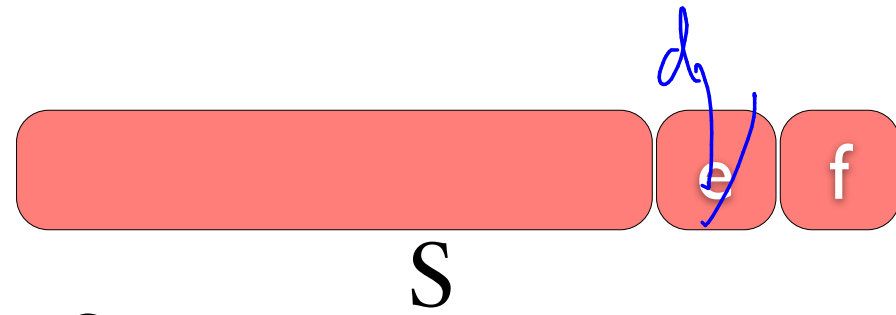
$\Rightarrow S'$ agrees with S_{ff} and has $\#misses(S)$

easy case 2

$S_{pse} \not\subseteq \text{cache}$, but both S and S_{ff} "evict e for d "

Same arguments apply from case 1.

Proof of lemma



case 3 $d \notin \text{cache}$, S evicts e .

S_{ff} evicts f .

We need to construct a schedule S' that satisfies

- ① agrees w/ S_{ff} on $j+1$
- ② has same # of misses as S

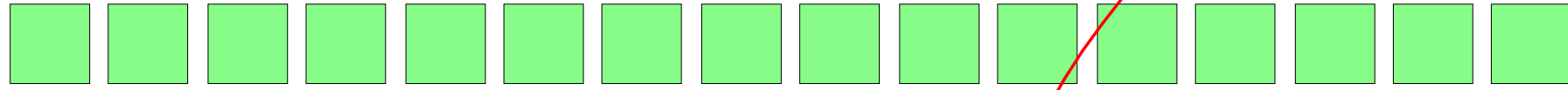
Timeline

t

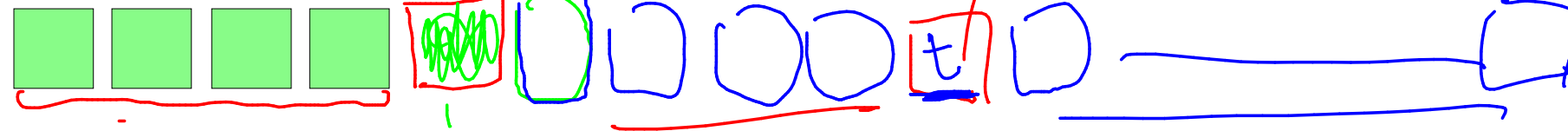
first

operation in S
that involves
either e or f .

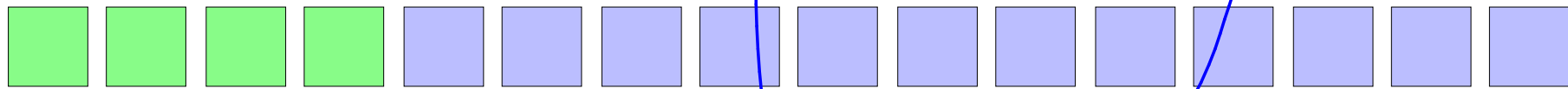
S_{ff}



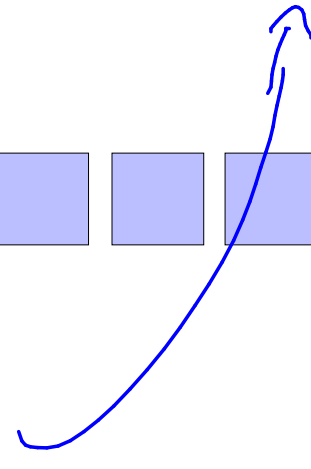
S'



S



copy from S



Proof of lemma

S [red bar] d f

S' [red bar] e d

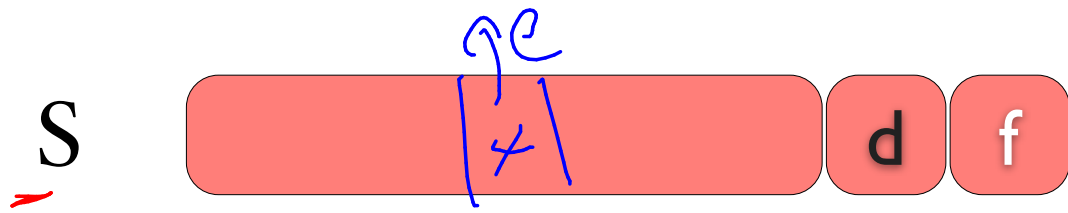
Let access t be the first operation in S after $j+1$ that involves either e or f .

Either $t = e$

$t = f$

$t = g \neq e \neq f$

Proof of lemma



what if $g=e$? S must load e.
"Evict x for e"
b/c this is the first
operation after $j+1$ that
involves e or f



S' can issue the operation
"evict x for f"

as a result, S' and S will have the
same state of the cache and
therefore the same # of misses.

Return Reduce(S')

Proof of lemma

S  d f

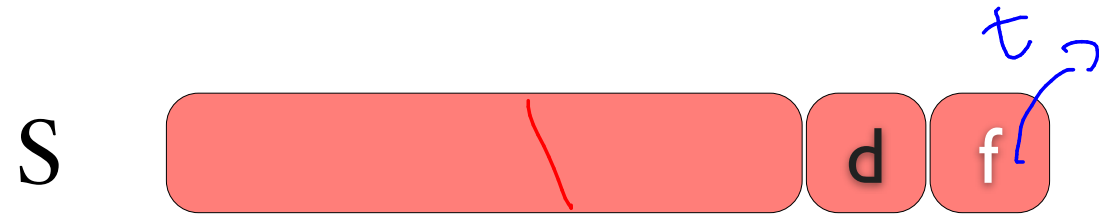
S'  e d

what if ~~f~~g=f?

Cannot happen. Because S_{ff} uses the

"farthest in the future" rule, so f cannot
be accessed before e.

Proof of lemma



what if ~~t~~ is neither e nor f?

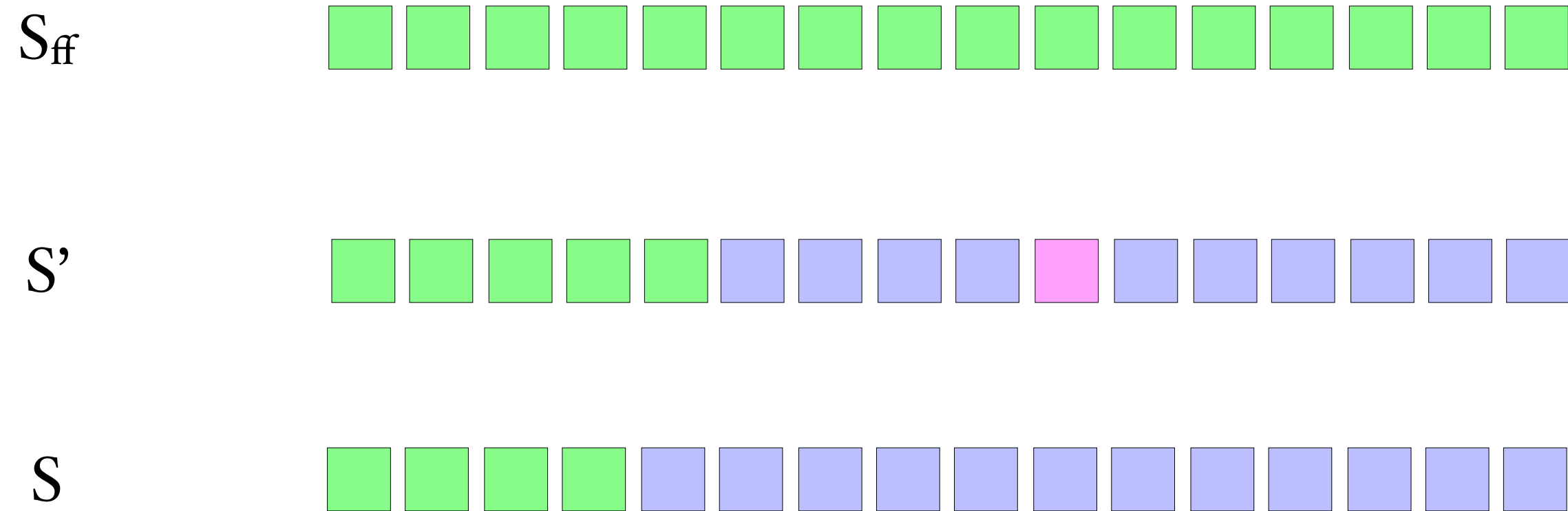
"evict f for t"



"evict e for t"

\ /
caches will be the same again.

What have we shown



Let S be a reduced sched that agrees with S_{ff} on the first j items.
There exists a reduced sched S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

Let S be a reduced sched that agrees with S_{ff} on the first j items.
There exists a reduced sched S' that agrees with S_{ff} on the first $j+1$ items and has the same or fewer #misses as S .

S^*

S_{ff}